



XA-9574  
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hideki YASUOKA et al.

Appln. No.: 09/989,061

Group Art Unit: 2812

Filed: November 21, 2001

Examiner: J. Kennedy

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD  
OF MANUFACTURING THE SAME

PETITION FOR EXTENSION OF TIME

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

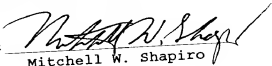
It is respectfully requested that the time now set for response be extended, retroactively, by three months. The required petition fee for a large entity is submitted herewith. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper to Deposit Account No. 50-1165.

Respectfully submitted,

MWS:lmb

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By:

  
Mitchell W. Shapiro  
Reg. No. 31,568

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on August 4, 2003.

  
Mitchell W. Shapiro